HPC Project launches v 1.4 Par4All

Paris May 15th, 2012 - HPC Project introduces the 1.4 version of Par4All open parallelization platform extending its scope of processing and generating CUDA and OpenCL code.

Par4All is the industrial implementation of a technology developed by the teams of CRI/Mines ParisTech and Télécom Bretagne. Its goal is to allow industrial users to meet the challenge of multi-cores architectures and other parallel processors for generic or embedded systems. From a C or FORTRAN application, Par4All automatically generates a parallel code to OpenMP, CUDA (compilable on NVIDIA GPU) and OpenCL. The generated code is readable and completely traceable with the original code. The whole process works like a usual compilation.

This new version introduces enhancements for loop processing for CUDA and OpenCL kernel generations. Moreover, dependencies resulting from accesses to global variables are now more finely analyzed to assess parallelism.

More on Par4All at www.par4all.org

About HPC Project
HPC Project was established in 2007. HPC Project is a pioneer in developing tools and strategies for high performance computing and code optimization. HPC Project goal is to bring the power of supercomputer on the engineer's desk.

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