**HPC Project announces Wild Cruncher, solution for High Performance numerical simulation**

**Meudon la Forêt june 14th 2011**, - HPC Project announces its new High Performance appliance, Wild Cruncher, dedicated to numerical simulation users.

The primary goal for Wild Cruncher is to bridge the gap between prototyping and production phases for system engineering. This is made possible through code analysis and optimization technologies on which HPC Project has based its success.

Usually, engineers use an environment like Scilab for system modeling. They simulate the model through dynamic execution of scripts. The Scilab environment is very flexible and easy to use. However, this comes at the cost of performance when there is a need for high data volume processing or deployment of the model once it has been validated. In this case, a complete rewriting of the model in a more performing language like C is necessary. This has a cost; it is time consuming and creates a control gap between the engineer and the programmer. For these reasons this rewriting phase is often skipped except for critical projects.

With Wild Cruncher, this rewriting is automated: once the model is validated, a simple “click” will generate the C version. Another “click” will generate a parallel version in order to take advantage of multi-core or hybrid hardware architectures. All these phases, including the compiled code execution, take place in a single working homogeneous environment.

When using Wild Cruncher performance gains are impressive: up to several hundred times the basic performance.

Wild Cruncher is a combination of hardware and software. The Scilab to C translation software is a specific development from HPC Project. Parallelisation, code generation and optimized compilation for the associated hardware are performed with Par4All, open source software, supported and promoted by HPC Project. The hardware is built with the most performing technologies from Intel and NVIDIA embedded in an office environment compatible device.

Wild Cruncher is available and will be demonstrated during the Forum Teratec 2011, http://www.teratec.eu/forum/index.html

*HPC Project was established in December 2007. HPC Project is a pioneer in developing tools and strategies for high performance computing and code optimization. HPC Project goal is to bring the power of supercomputer on the engineer's desk.*

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